

TOTAL MEMORY	BANK 0 (30-PIN)	BANK 1 (72-PIN)	BANK 2 (72-PIN)
21MB	256K x 4	4M x 1	16M x 1
	256K x 4	16M x 1	4M x 1
	1M x 4	1M x 1	16M x 1
	1M x 4	16M x 1	1M x 1
24MB	4M x 4	1M x 1	4M x 1
	4M x 4	4M x 1	1M x 1
	1M x 4	4M x 1	16M x 1
	1M x 4	16M x 1	4M x 1
32MB	4M x 4	16M x 1	16M x 1
		16M x 1	16M x 1
		32Mx1*	32Mx1*
33MB	256K x 4	16M x 1	16M x 1
	4M x 4	1M x 1	16M x 1
	4M x 4	16M x 1	1M x 1
	1M x 4	16M x 1	16M x 1
36MB	4M x 4	4M x 1	16M x 1
	4M x 4	16M x 1	4M x 1
	4M x 4	16M x 1	16M x 1
	4M x 4	16M x 1	16M x 1
48MB	4M x 4	16M x 1	16M x 1
	16M x 4		
64MB	16M x 4	32Mx1*	32Mx1*
65MB	16M x 4	1M x 1	1M x 1
66MB	16M x 4	1M x 1	1M x 1
68MB	16M x 4	4M x 1	4M x 1
69MB	16M x 4	1M x 1	4M x 1
	16M x 4	4M x 1	1M x 1

\* Double-RAS SIMM

Table 3-1. DRAM Configurations (Continued)

486-GIO-VP

TOTAL MEMORY	BANK 0 (30-PIN)	BANK 1 (72-PIN)	BANK 2 (72-PIN)
72MB	16M x 4	4M x 1	4M x 1
80MB	16M x 4	16M x 1	
81MB	16M x 4	1M x 1	16M x 1
84MB	16M x 4	4M x 1	16M x 1
96MB	16M x 4	16M x 1	16M x 1

Table 3-1. DRAM Configurations

→ NOTE : When using double-sided SIMM, it is advised that Bank 2 be used instead of Bank 1. If Bank 1 contains a double-RAS SIMM, then Bank 0 (30-pin) is rendered inoperative.

### Installation Instructions

→ NOTE : Always observe static electricity precautions. See "Handling Precautions" at the start of this manual.

1. Locate the SIMM banks on the mainboard. Determine your desired configuration to be installed.
2. Insert the SIMM edge connector at a 90-degree angle onto the socket.

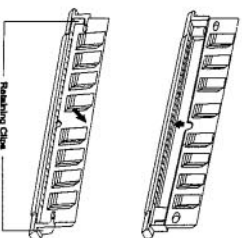


Figure 3-2. Installing SIMMs

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